

Bug Advisory

Title	ESP32-C5 Series Chips Bug Advisory
Issue Date	2026/02/26
Advisory Number	AR2025-008
Serial Number	NA
Version	V1.0

Issue Summary

The following issues have been identified in the ESP32-C5 series chips:

Issue 1: PSRAM Reset Hang

When ESP32-C5 series chips run ESP-IDF v5.5.1 with PSRAM enabled, CPU or digital reset operations may hang. This triggers a secondary RTC WDT reset. If the rollback feature (`CONFIG_BOOTLOADER_APP_ROLLBACK_ENABLE`, disabled by default) is enabled, this sequence may result in an OTA rollback.

Issue 2: AES and SHA Access to PSRAM

When ESP32-C5 chips run ESP-IDF v5.5.1, PSRAM data may be corrupted when AES or SHA hardware accesses buffers that are not aligned to a 16-byte boundary.

Issue 3: Sleep Coexistence Stability

When ESP32-C5 series chips run ESP-IDF v5.5.1 with `ESP_WIFI_ENHANCED_LIGHT_SLEEP` enabled, task watchdog timeouts may occur during Wi-Fi/BLE/IEEE 802.15.4 dual-mode or tri-mode coexistence, and the system may fail to recover after a CPU reset.

Impact Analysis

Affected Product Series:

These issues affect the ESP32-C5 series of chips.

ESP-IDF Affected Versions:

Issue 1: PSRAM Reset Hang

ESP-IDF Branch	Affected Commit IDs	Affected ESP-IDF Versions
master	Commits before c467c91 (excluded)	No official release yet
release/v6.0	Commits before 3df1ee1 (excluded)	v6.0-beta1
release/v5.5	Commits before ddb9f5d (excluded)	v5.5.1

Issue 2: AES and SHA Access to PSRAM

ESP-IDF Branch	Affected Commit IDs	Affected ESP-IDF Versions
master	Commits before da1ff70 (excluded)	No official release yet
release/v6.0	Commits before 145de94 (excluded)	v6.0-beta1
release/v5.5	Commits before 86bd3d3 (excluded)	v5.5.1

Issue 3: Sleep Coexistence Stability

ESP-IDF Branch	Affected Commit IDs	Affected ESP-IDF Versions
master	Commits before abd35ae (excluded)	No official release yet
release/v6.0	Commits before e1d2625 (excluded)	v6.0-beta1
release/v5.5	Commits before e3d0b19 (excluded)	v5.5.1

Note: ESP32-C5 is supported starting from ESP-IDF v5.5.1.

Mitigation

ESP-IDF Patched Versions:

Issue 1: PSRAM Reset Hang

ESP-IDF Branch	Fixed Commit IDs	Fixed ESP-IDF Versions
master	c467c91	v6.1
release/v6.0	3df1ee1	v6.0
release/v5.5	ddb9f5d	v5.5.2

Issue 2: AES and SHA Access to PSRAM

ESP-IDF Branch	Fixed Commit IDs	Fixed ESP-IDF Versions
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master	da1ff70	v6.1
release/v6.0	145de94	v6.0
release/v5.5	86bd3d3	v5.5.2

Issue 3: Sleep Coexistence Stability

ESP-IDF Branch	Fixed Commit IDs	Fixed ESP-IDF Versions
master	abd35ae	v6.1
release/v6.0	e1d2625	v6.0
release/v5.5	e3d0b19	v5.5.2

Recommendations for Application Developers

If your application is affected by the issues described above, and you are using an affected ESP-IDF version, it is recommended to upgrade to the patched ESP-IDF version (see the table above). If you encounter any issues during the upgrade process, please provide [Espressif](#) with the ESP-IDF version number or Commit ID. We will confirm and handle related matters as soon as possible.